

MAPPING CIRCUITRY AND METHOD

[ABSTRACT OF THE DISCLOSURE]

Mapping circuitry (40) comprises a first candidate
output value producing unit (42) which produces a first
5 candidate output value (C1) that differs by a first
offset value (x) from a received input value (r). A
second candidate output value producing unit (44)
operates, during operation of the first candidate
output value producing unit (42) to produce the first
10 candidate output value (C1), to produce a second
candidate output value (C2) that differs by a second
offset value (y) from the received input value (r).
The first and second offset values (x, y) are such that
a difference between them is equal to a difference
15 between respective output-range limit values defining
the limits of a preselected range of allowable values,
and such that, for any input value (r) within a
preselected range of allowable input values, one of the
first and second candidate output values (C1, C2) is
20 within the preselected output-value range and the other
of those two values is outside that range. An in-range
value determining unit (46) determines which one of the
first and second candidate output values (C1, C2) is
within the preselected output-value range, and an
25 output value selection unit (48) selects as the output
value (p) corresponding to the received input value (r)
that one of the first and second candidate output
values (C1, C2) which is determined to be within the
output-value range.

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[Fig. 10]